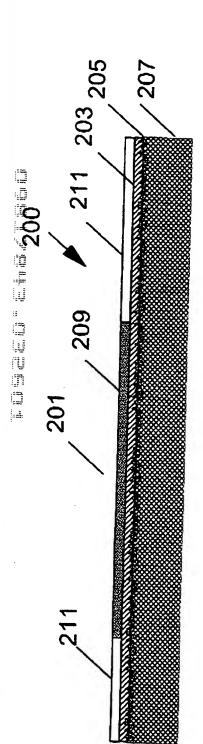
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Fig. 1 Zoom of laminated copper foil to dielectric surface



Prior art #1 subtractive:

Fig. 2 Apply photoresist / expose / develop

















Prior art # 1 subtractive:

Fig. 3 After etching & strip photoresist - BGA pad formed

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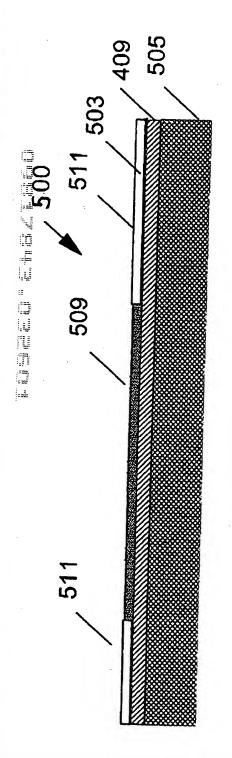
TR Miller

Current invention embodiment #1 subtractive:

Fig. 4 laminated copper foil to dielectric surface dendritric side up

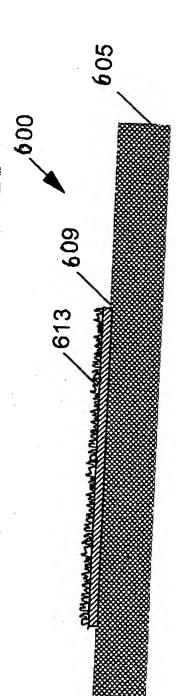
4/19 (END000008US1)

TR Miller



Current invention embodiment #1 subtractive:

Fig. 5 Apply photoresist / expose / develop



Current invention embodiment #1 subtractive:

Fig. 6 After etching & strip photoresist - BGA pad formed

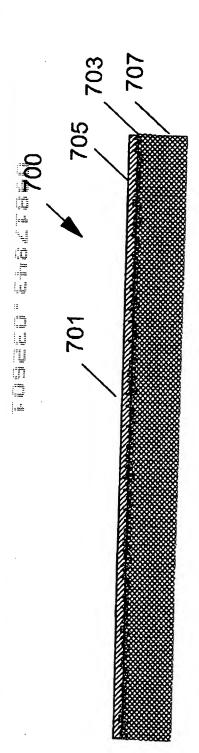


Fig. 7 Zoom of laminated copper foil to dielectric surface



Current invention embodiment #2 additive pattern plate:

Fig. 8 Surface copper etched off

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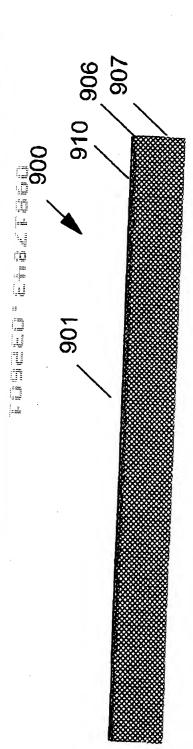


Fig. 9 Surface topography changed by plasma, vapor blasting or other chemical / mechanical attack

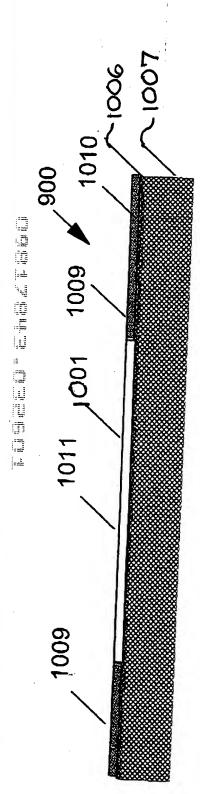


Fig. 10 Apply photoresist / expose / develop

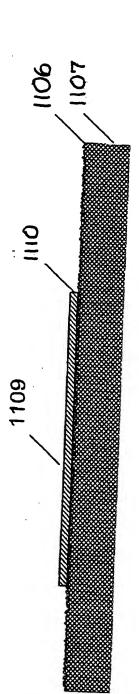
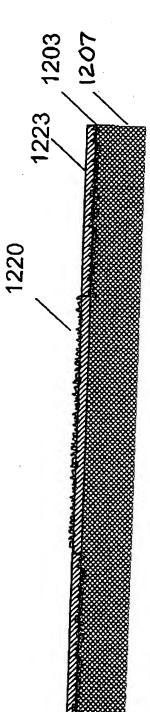


Fig. 11 Copper plate / strip photoresist / remove catalyst layer (optional)

TR Miller



Current invention embodiment #3 subtractive:

Fig. 12 Zoom of laminated copper foil to dielectric surface

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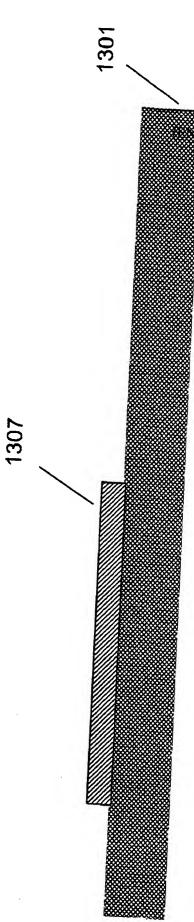
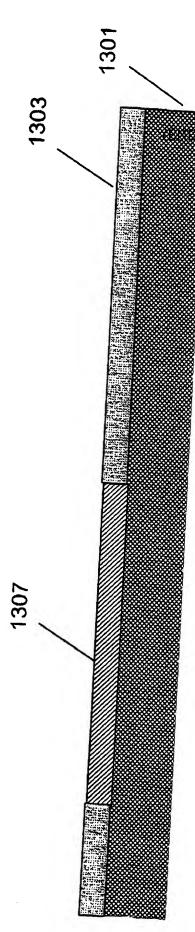


Fig. 13 - BGA 'spring-like" structure



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Fig. 14 - BGA 'spring-like" structure

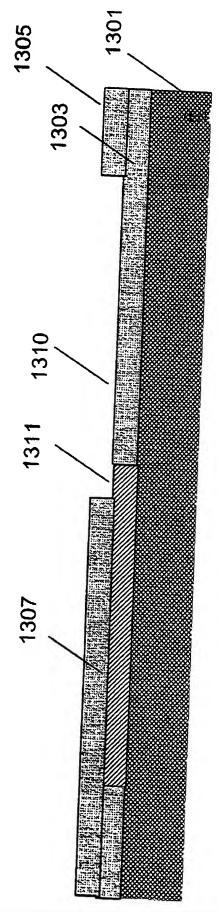


Fig. 15 - BGA 'spring-like" structure



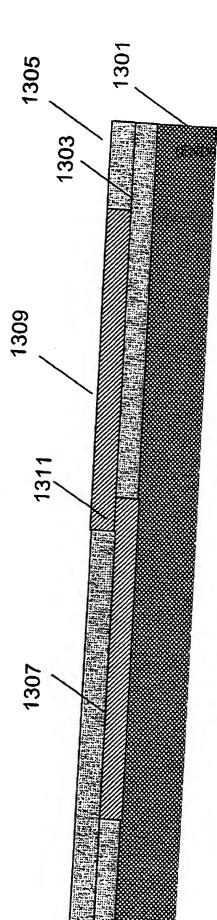


Fig. 16 - BGA 'spring-like" structure

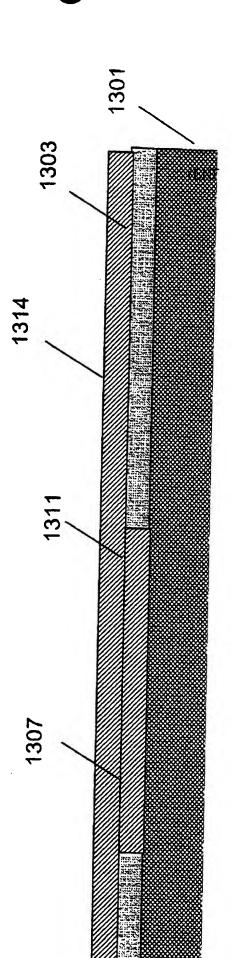


Fig. 17 - BGA 'spring-like" structure

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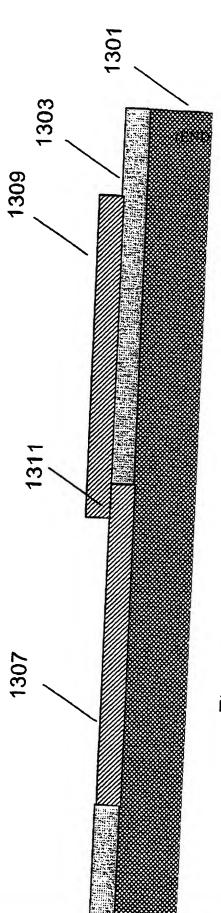
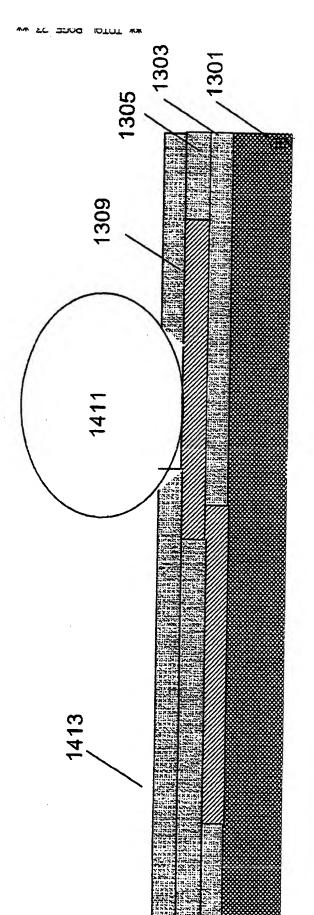


Fig. 18 - BGA 'spring-like" structure



<u> Pubecu chazeva</u>

Fig. 19 - BGA 'spring-like" structure